



JA
XA-9665A
PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

Hiroshi AKASAKI et al.

Appln. No.: 10/823,664 Group Art Unit: 2824

Filed: April 14, 2004 Examiner: V. Nguyen

For: SEMICONDUCTOR INTEGRATED CIRCUIT HAVING PROGRAMMABLE DELAYS FOR GENERATING TIMING SIGNALS WITH TIME DIFFERENCE BEING NON-INTEGRAL MULTIPLE OF CLOCK CYCLE

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SUPPLEMENTAL AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Supplementing the Amendment filed February 6, 2006,
please amend the above-identified patent application as
indicated below.

Amendments to the claims are reflected in the listing
of claims which begins on page 2 of this paper.

Remarks begin on page 5 of this paper.